

	L #	Hits	Search Text	DBs	Time Stamp
1	L75	1	"20030174454"	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 15:35
2	L80	1	175 and header same hybrid adj2 subassembly same driver same trench	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 15:47
3	L81	7	(("5542018") or ("5638391") or ("6081638") or ("6205274") or ("6559227") or ("6282005") or ("6638391")).PN.	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 15:50
4	L82	128	laser and (driver or control\$4) and (submount or subassembly) and trench	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:06
5	L87	28	laser same trench same driver	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:10
6	L88	45	chip same trench same driver	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:10
7	L89	42	88 not 87	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:11

	L #	Hits	Search Text	DBs	Time Stamp
8	L90	6	89 and laser	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:14
9	L91	2080	(372/34,36) .CCLS.	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:15
10	L92	18	91 and trench and chip	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:19
11	L93	24	91 and trench	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:19
12	L94	6	93 not 92	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:20
13	L95	29	(laser or LD adj chip) with trench same (driver or driving)	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:34
14	L104	43	(laser or LD adj chip) same trench same (driver or driving)	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:34

	L #	Hits	Search Text	DBs	Time Stamp
15	L105	288	(laser or LD adj chip) same trench same (driver or driving or control\$4)	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:40
16	L106	259	105 not 95	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 16:35
17	L107	44	(laser or LD adj chip) with trench and (driver or driving) adj2 circuit\$3	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:17
18	L121	74	(laser or LD adj chip) with (driver or driving) adj2 circuit\$3 with separate\$	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:17
19	L122	10	(laser or LD adj chip) with (driver or driving) adj2 circuit\$3 with separate\$ and (submount or mount)	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:19
20	L123	5	(laser or LD adj chip) with (driver or driving) adj2 circuit\$3 with isolat\$4 and (submount or mount)	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:20
21	L124	29	(laser or LD adj chip) with (driver or driving) adj2 circuit\$3 with isolat\$4	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:23

	L #	Hits	Search Text	DBs	Time Stamp
22	L125	24	124 not 123	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:20
23	L126	188	(laser or LD adj chip) and (driver or driving) adj2 circuit\$3 with isolat\$4	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:23
24	L127	164	126 not 125	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:24
25	L128	116	127 and (heat or thermal\$4)	USPA T; US-P GPUB ; EPO; JPO	2004/07/2 4 17:24